

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1666655
<b>Application Number:</b>	10699764
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	4234
<b>Title of Invention:</b>	In-circuit configuration architecture for embedded configurable logic array
<b>First Named Inventor/Applicant Name:</b>	Albert Sun
<b>Customer Number:</b>	22470
<b>Filer:</b>	Mark Andrew Haynes/diane bergin
<b>Filer Authorized By:</b>	Mark Andrew Haynes
<b>Attorney Docket Number:</b>	MXIC 1520-1
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		efsROA4.pdf	233792	yes	7

	<b>Multipart Description/PDF files in .zip description</b>		
	<b>Document Description</b>	<b>Start</b>	<b>End</b>
	Amendment - After Non-Final Rejection	1	1
	Applicant Arguments/Remarks Made in an Amendment	2	7
<b>Warnings:</b>			
<b>Information:</b>			
<b>Total Files Size (in bytes):</b>		233792	
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